

Ms.Vidya Ravindra Sagvekar

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Professional Objective Seeking a challenging environment that encourages continuous learning and creativity, provides exposure to new ideas, and stimulates personal and professional growth in the areas of Teaching.

Professional Experience Working as Assistant Professor in K.J.Somaiya Institute of Engineering and Information Technology,Sion,Mumbai since July 2008 to till date Worked as Lecturer in Xavier Institute of Engineering for one year.

Industrial Experience Worked as Technical Consultant in Bhabha Atomic Research Center for ten months.

Education Qualification

Pursuing PhD in Electronics & Telecommunication engineering from Mukesh Patel School of Management and Engineering,NMIMS University.

M.E.(Electronics & Telecommunication) : Masters Degree in Electronics & Telecommunication Engineering from Vidyalkar Institute of Technology,Wadala, Mumbai University in March 2013 with 73%.

B.E.(Electronics) : Bachelors of Electronics Engineering from D.Y.Patil Institute of Engineering & Technology,Pimpri,Pune University, in May 2004 with 63.66%.

Diploma in Industrial Electronics from Government Polytechnic,Bandra,Mumbai in June 2001 with 77%.
S.S.C. from Mahatma Gandhi Vidya Mandir,Bandra,Mumbai with 82%.

Skills Profile - Operating System : Windows 7 , Linux - Languages: C,C++, HDL -Tools and Packages : Microsoft office, Matlab, Xilinx, Sugar, Cadence P-spice, Microwind, Mentor Graphics HEP2

Online Exams NPTEL certification course on “ VLSI Design Verification and test” July to October 2016.

Subject Taught for UG: Digital System Design-I,Digital System Design-II,VLSI Design, Wireless Communication,Mechatronics, Microprocessor and Microcontroller- I ,Microprocessor and Microcontroller-II, Electronics Instrumentation System, Digital Circuits and Design, Microprocessor and Peripherals, Basics of VLSI Design, MEMS Technology,