Sejal Shah

Assistant Professor

Sejal Shah

2/11, Brahmajyoti, 361/C, Chandavarkar Road Matunga (C.R), Mumbai-400019

9869340474 sshah@somaiya.edu

Areas of Interest

Digital Signal Processing

Database Management Systems

Microprocessors

Experience

K. J. Somaiya Institute of Engineering and Information Technology, Sion, Mumbai - 400022.

March 2007 - PRESENT

As an Assistant Professor in Electronics Engineering Department.

Education

K. J. Somaiya College of Engineering / M. E. Electronics

August 2010 - September 2012, Mumbai

Scored 77% (I class with Distinction)

Fr. Conceicao Rodrigues College of Engineering / B.E. Electronics

September 2003 - June 2006, Mumbai

Scored 67% (I class)

Vivekanand Education Society Polytechnic / Diploma (Electronics

and Communication Engineering)

August 2000 - June 2003, Mumbai

Scored 78% (I class with Distinction)

Matunga Premier School / S. S. C

June 1991 - March 2000, Mumbai

Scored 86% (I class with Distinction)

Roles & Responsibilities

- As Class Incharge
- Member of Departmental Advisory Board.
- Member of Departmental Quality Circle
- Member of Program Assessment Committee
- Departmental member of Research & Development Committee
- Departmental member of NPTEL committee
- Departmental member of eYantra Team
- Departmental member of Cyber Security and Research Cell
- Departmental member of Eradication of Sexual Harassment (Vishakha)
- Member of F. E. Admission Committee (2015-2017)

- Mentor for NPTEL Online Certification Course on
 - Programming Data Structures and Algorithms
 - Digital Image Processing
 - Discrete Time Signal Processing
 - Developing Soft Skills and Personality
- As paper setter for KJSCOE for Signals and Systems and Digital Signal Processing and Processors

Awards

- Received a Minor Research Grant of Rs. 25, 000/- for "Home Automation using J. A. R. V. I. S" from University of Mumbai in 2013-14.
- Received a certificate and cash prize of Rs. 1000/- for successfully completing Task Based Training by eYantra, IIT-B in 2016
- Received a certificate for mentoring students' project "Automatic Car Battery Indicator for TIIIC- 2015. The group qualified for quarter finals.
- Received "Best Teacher Award in Electronics Engineering Department" in Surge 2017
- Received Minor Research Grant as Co-Investigator of Rs. 35,000/for the project "Low Cost Self Assistive Automated Wheelchair for Disabled People" in 2016-17 from University of Mumbai
- Received letter of appreciation for:
 - Organizing FUP on "Embedded Systems Design using ARM Cortex M3"
 - Conducting lectures for M. Sc. (Computer Science) for Digital Signal Processing in September 2013
 - Conducting guest lecture in VIT, Wadala, on "Speech Processing" on 20th October 2011.
 - Excellent performance in M. E. and classroom teaching.
 - Achieving 100% result in Digital Image Processing for two consecutive years (2015-17)
 - Organizing student seminars and workshop for International Conference on Advances in Science and Technology (ICAST) on 7th-8th April 2018
 - Distinguished contribution for teaching learning process for Digital Signal Processing and Processors (2017-18)
- Top 5 % Topper in NPTEL Certification Course on "Digital Image Processing" (88%) July-Oct 2016
- Top 10% topper in NPTEL Certification Course on "Discrete Time Signal Processing" (80%) July September 2016
- Top Performing Mentor for the online course "Developing Soft Skills and Personality" by NPTEL July - December 2017
- Mentored a students' collaborative project titled "Surface Texture Detection" with Reva Labs in 2017-18.

Courses

- STTP on "Laboratory Tools and Practices in Microcontrollers and Signal Processing" from 3rd-13th July 2007 at KJSCOE
- Two day workshop on "Speech Processing Techniques" from 6th 7th January 2012
- STTP on "NS2: A Helicopter View" from 5th- 9th November 2012.

- Faculty Updation Program on "Embedded System Design using ARM Cortex M3" from 29.12.2014- 02.01.2015
- One day National Symposium on "Evangelizing Internet Of Things" on 5th October 2015
- One day "NPTEL Awareness Workshop" on 8th January 2016
- Participated in two day workshop on "Introduction to Robotics" on 19th- 20th February 2016, at IIT-B
- One week training cum workshop on "NAAC-Process Criteria and Audit" from 25th - 29th April 2016
- Faculty Development Program on "IoT: Applications and Developments" from 2nd- 7th May 2016.
- Task Based Training (TBT 2016) conducted as a part of Teacher Training through eYantra Lab Setup Initiative in June 2016
- One day workshop on NPTEL for SPOCs at IIT-B on 5th July 2016
- NPTEL Online Certification Course on:
 - o Discrete Time Signal Processing (September 2016)- 80%
 - October 2016) -88%
 - Medical Image Analysis (April 2017) 76%
 - Digital Image Processing of Remote Sensing Data (August 2017) - 72 %
 - Oct 2018)-81%
- Two days National Symposium of eYantra at IIT-B on 7-8th April 2017
- Two day Saptahaant Shikshak Prashikshan on "Introduction to Machine Learning" conducted by IIT-Kharagpur on 16th-17th December 2017 with excellent grade.
- One week ISTE approved STTP on "R Programming, Python and Perl" organized by University of Mumbai, IET and CMP Infotech from 2nd 7th January 2018.
- One day NPTEL workshop conducted on 2nd August 2018 by IIT Madras at IIT Bombay.
- Three days eYantra workshop at IIT-B from 7th-9th February 2019 on "Advanced Topics in Embedded Systems and Robotics (Python and IoT)"
- Participated in seminar on "NAAC Revised Accreditation Framework- New Initiatives and Reforms" on 15th February 2019

• "Speaker Recognition" in International Journal Global Technological Initiatives (IJGTI) March 2012

- "Fast Speaker Recognition using Efficient Feature Extraction Techniques" in International Journal of Computer Science and Network (IJCSN) ISSN: 2277-5420, Volume 2, Issue 1, February 2013
- "Rotary Automated Car Parking System" in International Journal of Engineering Science and Innovative Technology (IJESIT) Volume 4 Issue 2, ISSN: 2319:5967, pp(408-415), March 2015
- "Internet of Things: Boon or Bane: A Review" in International Journal of Computer Science and Network (IJCSN) ISSN: 2277-5420, Volume 4, Issue 6, December 2015.
- "Automatic Door Locking using Face Recognition" in International Journal of Engineering Technology Science and Research (IJETSR)

Publications

ISSN: 2394-3386, Volume 4 Issue 4, April 2017.

- "Automatic Door Locking using Face Recognition" in International Journal of Computer Engineering and Applications (IJCEA) ISSN: 2321-3469. Volume XI, Special Issue. April 2017
- "Hand Gesture Recognition for Deaf and Dumb using Matlab" in Advances in Computational Sciences and Technology. Volume 11 Special Issue pp 40-42.
- "Surface Texture Detection Calculating Area of Crack using Image Processing" Advances in Computational Sciences and Technology. Volume 11 Special Issue pp 40-42.

Personal

Date of Birth: 8th January 1984

Blood Group: O+ve

Languages known: English, Hindi, Marathi, Gujarati, Kutchi