

Somaiya Vidyavihar University

Name: Dr. Om Prakash Goswami		E-mail: o.g@somaiya.edu	
Contact No: +91-8527039565			
Department/Section: Electronics Engineering			
College: K J Somaiya College of Engineering			
DOJ Somaiya: 18 th August 2023	Career Experience: 09 Yrs	Industry Experience: Nil	Teaching Experience: 9Yrs
Present Academic Designation: Assistant Professor (Professor/Associate Professor/Assistant Professor)		Present Administrative Designation: (Principal/Vice-Principal/ Associate Dean/ HOD etc)	

Area of research/specialization and Courses Delivered
Research domain/interests/areas 1. Design of different discrete time systems (Differentiator/integrators/Filters) 2. Optimization 3. 2-D digital systems and Image Processing

Recognition as a teacher by any University	UG: Yes/No	PG: Yes/No	Ph.D : Yes/No
Details of Recognitions			
1.			

Education					
Examination	Name of the Degree	University/Board	Institute/College	Year	CPI/SPI/ %Marks
Ph.D	Ph. D.	University of Delhi	Faculty of technology, Netaji Subhas institute of Technology, University of Delhi.	2022	82.5
PG	M.Tech.	Maharishi Dayananda University, Rohtak	KIIT College of Engineering, Sohna, Gurgaon	2013	76.77
UG	B.Tech	Uttar Pradesh Technical University	GLA Institute of Technology & Management	2009	63.8
Diploma					
NET/SET/Other					

Notable Experience Details					
Sr. No	Name of the organization	Designation	Date of Joining	Date of Leaving	Experience (Years)
1.	GLA University	Assistant Professor	October 2021	January 2023	1.3 years
2.	Faculty of Technology (NSIT) University of Delhi	Teaching & Research Fellow	August 2017	March 2020	2.7 years
3.	KIIT College of	Assistant Professor	July 2013	June 2017	4 years

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	Engineering				
4.	KIIT College of Engineering	Lecturer	Jan 2010	April 2011	1.2 years

Research Accomplishments and Projects					
No of students pursuing Ph.D as on date: NIL			No of students completed Ph.D as on date: NIL		
No of students completed PG thesis / Project work as on date:4			No of students / groups completed UG projects as on date:15		
Publications Total:10		Number of Peer review Journal papers:5		Number of Conference papers:5	
Details of Publications: International Journals <ol style="list-style-type: none"> O. P. Goswami, T. K. Rawat, and D. K. Upadhyay, "A Novel Approach for the Design of Optimum IIR Differentiators Using Fractional Interpolation" Circuits System & Signal Processing 39(3), 1688–1698 (2020). DOI: 10.1007/s00034-019-01211-0 (Springer), Impact factor (2.225). Electronic ISSN 1531- 5878 Print ISSN 0278-081X O. P. Goswami, T. K. Rawat, and D. K. Upadhyay, "Design of Optimum IIR Integrators and Differentiators" International Journal of Electronics. (Taylor & Francis), 108(9), 1482-1496,(2021)DOI: 10.1080/00207217.2020.1870730, Impact factor (1.336), Print ISSN: 0020-7217 Online ISSN: 1362-3060 O. P. Goswami, D. K. Upadhyay and T. K. Rawat, "Extended Bilinear Transform and Multirate TechniqueBased Approach for Analog-to-Digital Transform" International Journalof Electronics. (Taylor & Francis), (2021) DOI: 10.1080/00207217.2021.1969446, Impact factor (1.336), Print ISSN: 0020- 7217 Online ISSN: 1362-3060 O. P. Goswami, D. K. Upadhyay, and T. K. Rawat, "L₁-Norm-Based Optimal Design of Digital Differentiator Using Multiverse Optimization," Circuits System & Signal Processing, 41(1), 4707-4715 (2022). DOI: 10.1007/s00034-022-02003-9, Impact factor (2.225). Electronic ISSN 1531-5878 Print ISSN 0278-081X O. P. Goswami, A. Shukla, M. Kumar, "Optimal Design and Low-noise Realization of Digital Differentiator," Journal of Electrical Engineering, 73(5), 332–336, (2022). Electronic ISSN 1339-309X Conferences <ol style="list-style-type: none"> O. P. Goswami, T. K. Rawat, and D. K. Upadhyay, "Fractional Bilinear Transform Based Design of Wideband Digital Differentiator Using Multirate Technique" 5th IEEE International Conference on Signal Processing, Computing and Control (ISPCC 2k19), Oct 10-12, 2019, JUIT, Solan, India, pp. 372-375. DOI: 10.1109/ISPCC48220.2019.8988493 O. P. Goswami, T. K. Rawat, and D. K. Upadhyay, "Design of Digital Wideband Integrator Using Optimized Fractional Bilinear Transform and Multirate Technique" IEEE Students Conference on Engineering & Systems (SCES) July 10-12, 2020, Prayagraj, India, pp. 1-5. DOI: 10.1109/SCES50439.2020.9236762 (Best Paper) O. P. Goswami, D. K. Upadhyay and T. K. Rawat, "Fractional Delay Approximation Based Design of Low-order Digital Differentiator" 2020 IEEE 5th International Conference on Computing, Communication and Automation (ICCCA) during October 30-31, NCR New Delhi, pp. 601-604. DOI: 10.1109/ICCCA49541.2020.9250917 S. Shrivastava, D. K. Upadhyay and O. P. Goswami, "Optimal Design of Fractional-order Low-pass Filter Using L₂-method," 2021 International Conference on Communication, Control and Information Sciences (ICCISc), 2021, pp. 1-5, DOI: 10.1109/ICCISc52257.2021.9484936 Books/Book Chapters <ol style="list-style-type: none"> SK Mishra, O. P. Goswami, N Kumar, N Kumar,"Computationally Efficient Fractional-order Filters" Applications of Computational Intelligence Techniques in Communications, 42-52 Patents/Copy Rights					

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1.		
No of Research / consultancy / projects completed: Rs:_____	No of Research / consultancy / projects on-going: Rs:_____	No of Research / consultancy / projects on applied as on date: Rs:_____
Details of Research / consultancy / projects: Completed: NIL		
On-going: NIL		
Applied: NIL		
IPR/ Copyrights: NIL		

FDPs/Seminars/Workshops/Training Programs Attended/ Organized/ Delivered
<p>Attended</p> <ol style="list-style-type: none"> 1. One-week Short Term Course on “Digital Signal Processing–using MATLAB”. organized at Netaji Subhas University of Technology (NSUT) from 3rd to 7th June, 2019. 2. One-week coordinators workshop on “Control systems” organized by IIT Kharagpur from 15th September to 19th September 2014 under “T10KT” program of National Mission on Education through ICT (MHRD, Govt. of India). 3. Workshop on “Signals & Systems” conducted by IIT Kharagpur, from 2nd to 12th January 2014, held under NMET through ICT (MHRD, Govt. of India). 4. Workshop on “Analog Electronics” conducted by IIT Kharagpur, from 4th to 14th June 2013, held under NMET through ICT (MHRD, Govt. of India).
<p>Organized</p> <ol style="list-style-type: none"> 1. “Workshop coordinator” of two-week ISTE workshop on CMOS, Mixed Signal and Radio frequency VLSI Design organized by IIT Kharagpur from 30th January to 4th February 2017 under National Mission on Education through ICT (MHRD, Govt. of India). 2. “Workshop coordinator” of two-week ISTE workshop on Control Systems organized by IIT Kharagpur from 2nd to 12th December 2014 under “T10KT” program of National Mission on Education through ICT (MHRD, Govt. of India). 3. “Coordinator” of National level workshop on Designing & Verification of digital VLSI system using HDL & on Embedded systems organized by Tevatron Technologies Pvt. Ltd. Noida at KIIT college of Engineering Gurugram.
<p>Delivered</p> <ol style="list-style-type: none"> 1.

Notable Key Scholastic Achievements	
1.	
2.	

Notable Positions and Responsibility	
1.	
2.	

Date: 01 / 01 / 2024

Member

Signature of Faculty